



ISCA-35

THE 35th ANNUAL INTERNATIONAL SYMPOSIUM ON COMPUTER ARCHITECTURE

June 21 – 25, 2008

Beijing, China

Sponsored by  ACM SIGARCH  IEEE TCCA

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The *International Symposium Computer Architecture* is the premier forum for discussing new ideas and experimental experiences in computer architecture. This year's conference will be held in Beijing, China, where technical discussions will mix with rich culture and history. Novel papers are solicited on a broad range of topics, including, but not limited to:

- Processor architectures
- Multiprocessor and chip multiprocessor architectures
- Memory systems
- Storage subsystems
- Interconnection Networks
- Hardware support for parallel programming
- Hardware support for debugging and program correctness
- Hardware support for reliability and fault tolerance
- Hardware support for security
- Hardware support for virtual machines
- Instruction level, thread level, and data level parallelism
- Power and energy efficient architectures
- Application specific, reconfigurable, and embedded architectures
- Network processor and router architectures
- Architectures for emerging technologies and applications
- Impact of technology on architecture
- Architecture modeling and simulation methodology
- Performance evaluation and measurement of real systems

IMPORTANT DATES:

Abstract Deadline: Nov 9th, 2007

Final Paper Deadline: Nov 19th, 2007

The final paper deadline already includes a 3-day automatic extension. No further extensions will be given. Please visit the conference link below for further details on submitting papers:

More detailed submission instructions can be found at the ISCA 35 web site.

<http://isca2008.cs.princeton.edu/>